

2017-2 (BIG ENDIAN)

Eg. **INTEGER32 = 0000 000F**; ADDRESS32 = 0080 000F

PAGE SIZE = 4 kbytes --> SPACE = 12 bits

ADDRESS32(00800 00F): **PageNumber=00800** **PageOffset=00F**

Physical Address (PA) space = 44 bits.

Page Table Entry (PTE) start address PA: 0001 0000 000

Page Number	PTE address	PTE	
00000	0001 0000 000	00 02 00 00	valid
00001	0001 0000 004	00 02 00 01	valid
00002	0001 0000 008	00 02 00 02	valid
00003	0001 0000 00C	00 02 00 03	valid
...	
00007	0001 0000 01C	00 02 00 07	valid
...	
00400	0001 0001 000	00 02 04 00	valid
...	
00800	0001 0002 000	00 02 08 00	valid
...	

FrameNumber = **00020800** ; FrameOffset = **00F**

Physical Address = **0002 0800 00F** **INTEGER32 = 0000 000F**

	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
0002 0800 00X																00
0002 0800 01X	00	00	0F													